AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning "In particular, the analog DC signal" on page 14 of the application with the following amended paragraph:

In particular, the analog DC signal voltage V3, which corresponds to an amplified signal of the inverting amplifying circuit 25 according to the first embodiment of the invention, is represented by the following equation 3:

$$V3 = -V2 \times Rt/(R22)$$
 [Equation 3]

where Rt represents the resistance of the temperature sensitive resistor R21resistor 21, and R22 represents the resistance of the resistor R22.